**Tutorial-8**

Q.1 How many T state are required in basic machine cycle given

* Op-code Fetch cycle
* Memory read cycle
* Memory write cycle
* I/O read cycle
* I/O write cycle

Also draw timing diagram of each of them.

Q. 2 If the Clock frequency of 8085 is 3.5 MHz; What is the Execution time for Instruction MVI B,05H.

Find (a) T state required for the instruction (b) time for memory read machine cycle and

(c) time for instruction cycle

Q.3 How many machine cycle and T states are required for

OUT C0H

STA 416B H

Also draw timing diagram for each of the instructions.

Q.4 Assuming the accumulator contains 26H and previous operation has set the carry flag. Add byte 57H to the accumulator. Also reflect status bit condition of the Flags: Sign, Carry, Auxuliary carry, Parity and Zero flag.